

David Holland

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EDUCATION

University of Virginia

Bachelor of Science, Computer Engineering

Charlottesville, VA

Expected May 2028

Relevant coursework: Digital Logic Design, Signals and Systems, Applied Circuits, Data Structures and Algorithms, Computer Systems and Organization I/II, Introduction to Embedded Systems, Electronics.

EXPERIENCE

University of Virginia Career Center — Data and Technology

Technology Assistant

Charlottesville, VA

Jan. 2026 – Present

- Imaged and deployed 50+ Windows laptops/workstations and macOS/iOS devices, standardizing configuration through Group Policy and Jamf Pro enrollment profiles to bring machines to a working state on first boot.
- Owned vulnerability management across 50+ endpoints via Qualys Patch Management, driving remediation that cut the office's open-vulnerability count by 30%.
- Served as first-line technical support for a 35-person office and redesigned the SharePoint intranet, restructuring navigation so staff could self-serve procedures and forms, keeping student-facing advising services running during peak season.

Freelance — Blackhawk Coaching

Independent Project

Richmond, VA

June. 2026 – Aug. 2026

- Delivered a custom knowledge-base application for Blackhawk Coaching as sole developer, scoping requirements with the client and translating their teaching material into a searchable self-service tool.

PROJECTS

Blackhawk AI — Full-Stack Retrieval-Augmented Generation System

Python, Flask, Supabase, PostgreSQL/pgvector

May 2026 – July 2026

- Built a Flask and Supabase RAG web application serving semantic search over a 700-chunk corpus, returning results in under 4 seconds per query via pgvector embeddings and multi-format document ingestion.
- Scaled a single-user CLI script into a deployed multi-user web app with Q&A and an authenticated admin panel.
- Cleared pre-launch security findings for stored XSS, debug-mode exposure, and missing HTTP security headers by adding input sanitization and hardening the Flask configuration.

8-Bit Central Processing Unit

VHDL, Intel Quartus II, RTL simulation

Jan. 2025 – May 2025

- Designed an 8-bit CPU in VHDL capable of arithmetic operations, logic execution, and data movement and storage.
- Built the datapath and control unit from combinational and sequential components, including an ALU, register file, multiplexers, decoders, counters, and timers.
- Verified module functionality through structured testbenches and RTL simulation, isolating logic faults before integration.

TECHNICAL SKILLS

Languages: Python, Java, VHDL, JavaScript, HTML, CSS, SQL

Frameworks & Tools: Claude, Claude Code, Flask, Supabase, pgvector, Git, Intel Quartus II, Autodesk Fusion

Systems & Platforms: Windows, macOS, Active Directory, Group Policy, Jamf Pro, Qualys, SharePoint