

David Holland

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EDUCATION

University of Virginia

Bachelor of Science, Computer Engineering

Charlottesville, VA

Expected May 2028

Relevant coursework: Digital Logic Design, Signals and Systems, Applied Circuits, Data Structures and Algorithms, Computer Systems and Organization I/II, Introduction to Embedded Systems, Electronics.

EXPERIENCE

University of Virginia Career Center — Data and Technology

Technology Assistant

Charlottesville, VA

Jan. 2026 – Present

- Imaged and deployed 50+ laptops and workstations, standardizing configuration through Windows Group Policy to bring new machines to a working state on first boot.
- Owned vulnerability management across 50+ endpoints by deploying Qualys Patch Management, triaging scan findings, and driving remediation of outstanding patches to reduce the office's open-vulnerability count.
- Redesigned and maintained the office SharePoint intranet, restructuring navigation and content so staff could locate internal procedures and forms without escalating to IT.
- Configured and deployed macOS and iOS devices through Jamf Pro, building enrollment and configuration profiles that standardized setup across the office fleet.
- Served as first-line technical support for a 35-person office, resolving hardware, account, and access issues to keep student-facing advising services running during peak season.

PROJECTS

Blackhawk AI — Full-Stack Retrieval-Augmented Generation System

May 2026 – July 2026

Python, Flask, Supabase, PostgreSQL/pgvector

- Built a Flask and Supabase RAG web application serving semantic search over a 500-chunk corpus, returning results in under 4 seconds per query via pgvector embeddings and multi-format document ingestion.
- Scaled a single-user CLI script into a deployed multi-user web app with anonymous Q&A and an authenticated admin panel.
- Cleared pre-launch security findings for stored XSS, debug-mode exposure, and missing HTTP security headers by adding input sanitization and hardening the Flask configuration.

8-Bit Central Processing Unit

Jan. 2025 – May 2025

VHDL, Intel Quartus II, RTL simulation

- Designed an 8-bit CPU in VHDL capable of arithmetic operations, logic execution, and data movement and storage.
- Built the datapath and control unit from combinational and sequential components, including an ALU, register file, multiplexers, decoders, counters, and timers.
- Verified module functionality through structured testbenches and RTL simulation, isolating logic faults before integration.

TECHNICAL SKILLS

Languages: Python, Java, VHDL, JavaScript, HTML, CSS, SQL

Frameworks & Tools: Flask, Supabase, PostgreSQL, pgvector, Git, GitHub, Intel Quartus II, Autodesk Fusion

Systems & Platforms: Windows, macOS, Active Directory, Group Policy, Jamf Pro, Qualys, SharePoint